

**Proceedings of the
20th USENIX Symposium on Operating Systems
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Errata Slip**

In the paper “MEGALON: Efficient Data Sharing for Partly Coherent CXL Memory” by Jiyu Hu, Seokjoo Cho, Landon Johnson, Kiran Hombal, and Shreesha Gopalakrishna Bhat, *University of Illinois Urbana–Champaign*; Marcos K. Aguilera, *NVIDIA*; Ramnatthan Alagappan and Aishwarya Ganesan, *University of Illinois Urbana–Champaign* (Wednesday session, “A Coherent, Consistent Session on Caching,” pp. 2281–2298 of the Proceedings), the authors have corrected reference 26:

Original text:

Yibo Huang, Haowei Chen, Newton Ni, Vijay Chidambaram, Dixin Tang, Emmett Witchel, Zhiting Zhu, and Zhipeng Jia. Tigon: A distributed database for a CXL pod. In Proceedings of the 19th USENIX Conference on Operating Systems Design and Implementation (OSDI '25), Boston, MA, July 2025.

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Yibo Huang, Haowei Chen, Newton Ni, Yan Sun, Vijay Chidambaram, Dixin Tang, and Emmett Witchel. Tigon: A distributed database for a CXL pod. In Proceedings of the 19th USENIX Conference on Operating Systems Design and Implementation (OSDI '25), Boston, MA, July 2025.