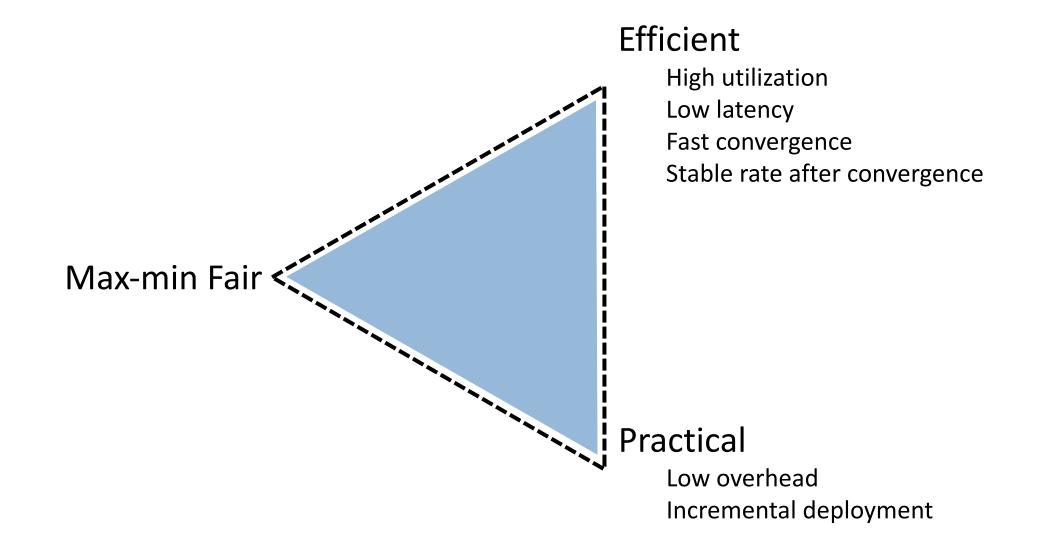
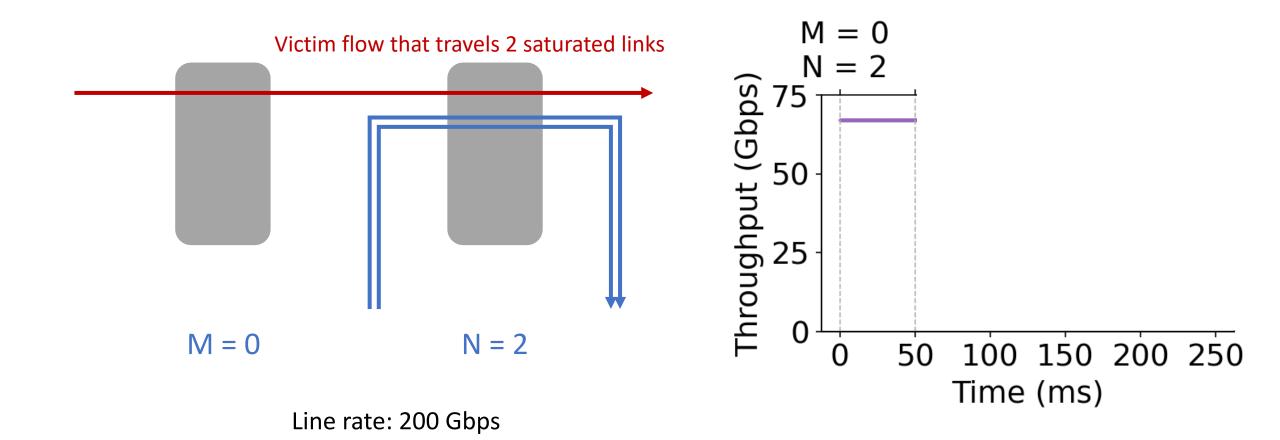
# Poseidon: Efficient, Robust, and Practical Datacenter CC via Deployable INT

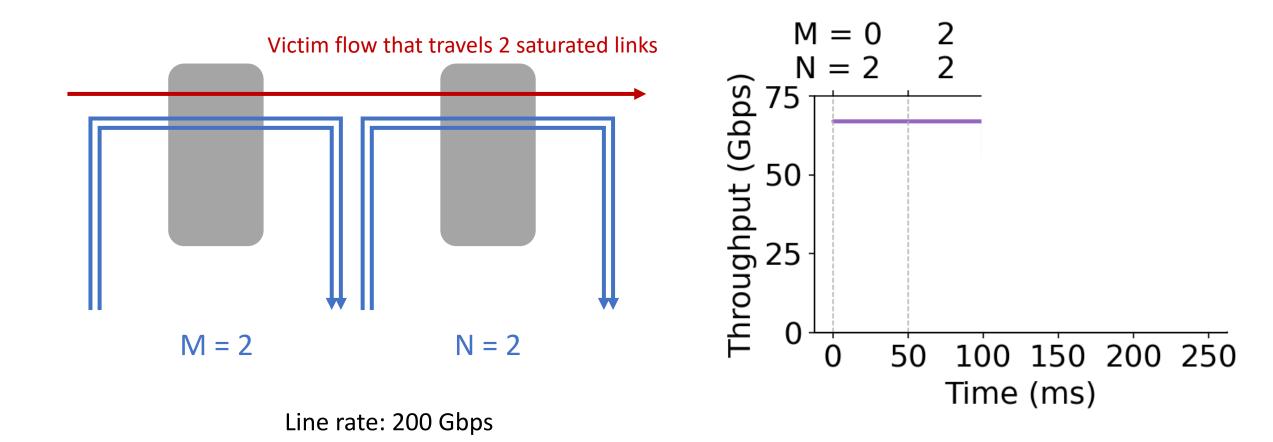
Weitao Wang, Masoud Moshref, Yuliang Li, Gautam Kumar, T. S. Eugene Ng, Neal Cardwell, Nandita Dukkipati

## A Good Congestion Control Algorithm

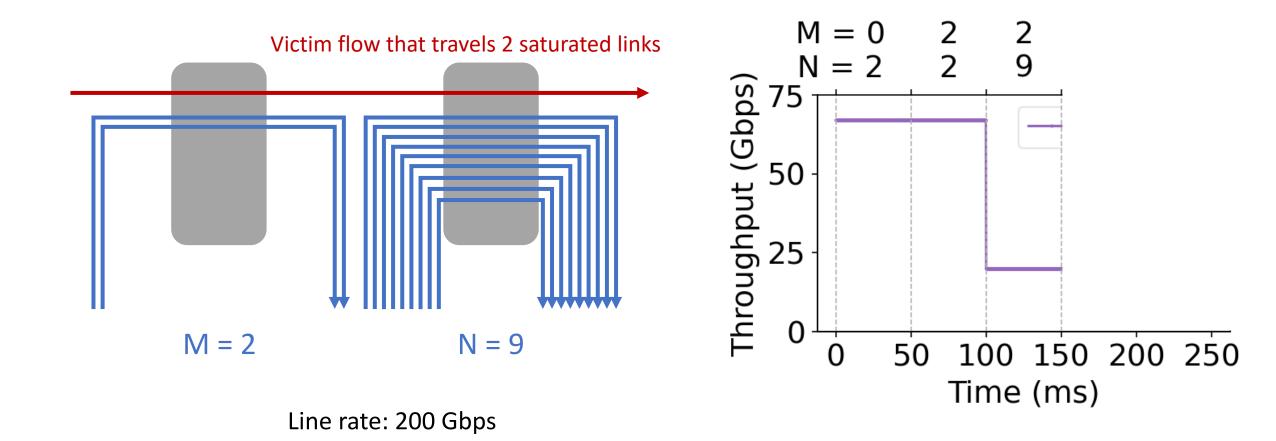




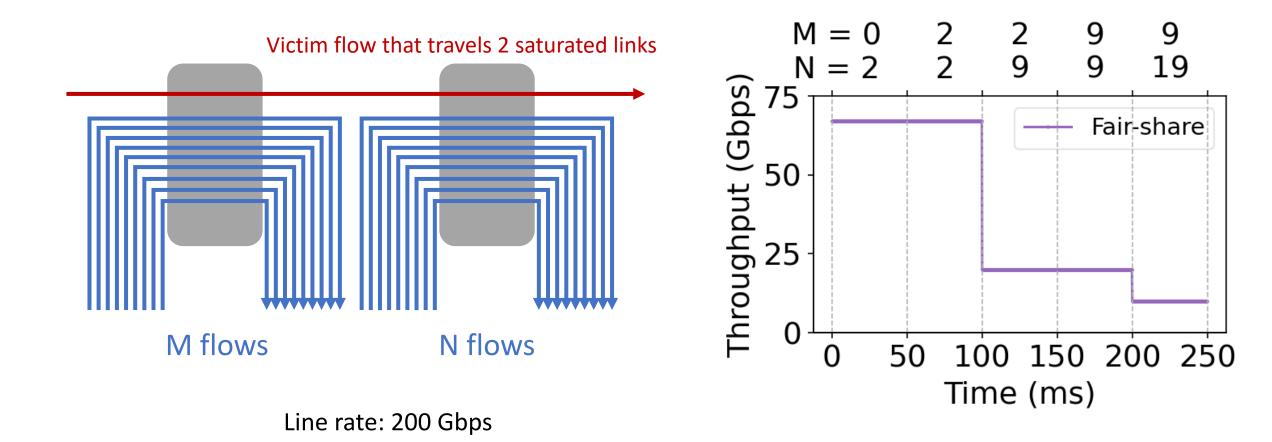
The fair-share for the victim flow changes when new flows join.



The fair-share for the victim flow changes when new flows join.



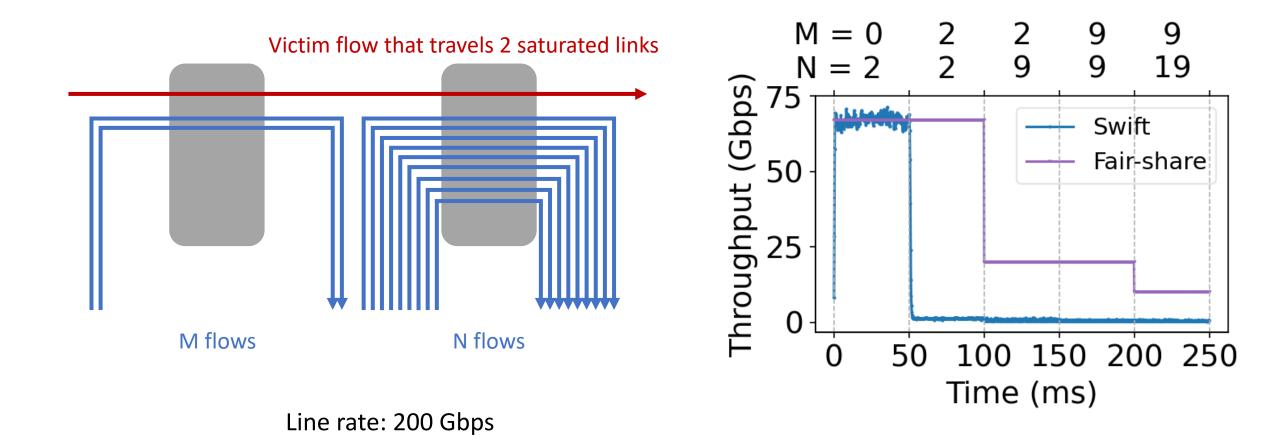
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The fair-share for the victim flow changes when new flows join.

Design Principles Motivation Key Idea Design Evaluation

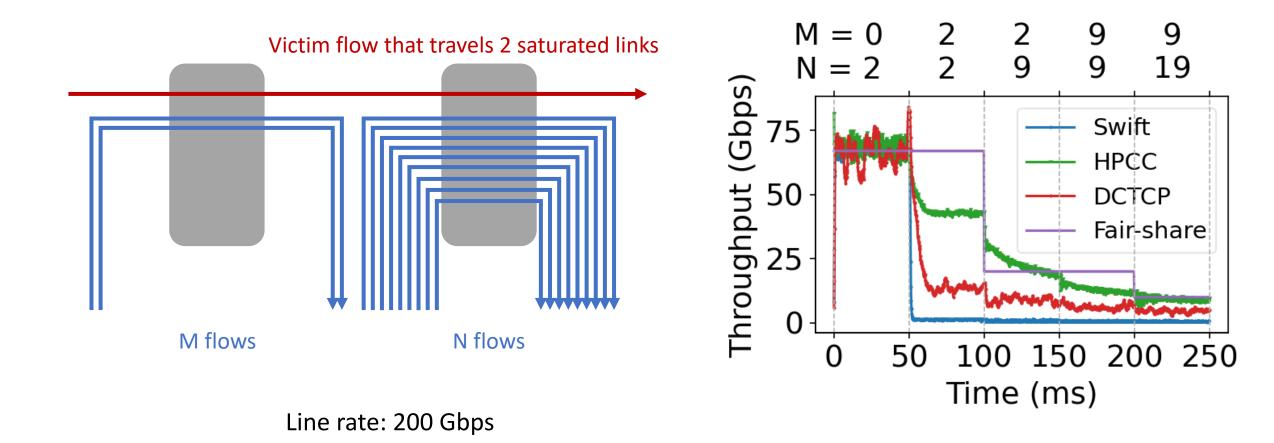
Motivation 1: React to Every Congestion -> Not Max-min Fairness



Swift reacts to end-to-end fabric delay, so the victim flow has a much higher fabric delay.

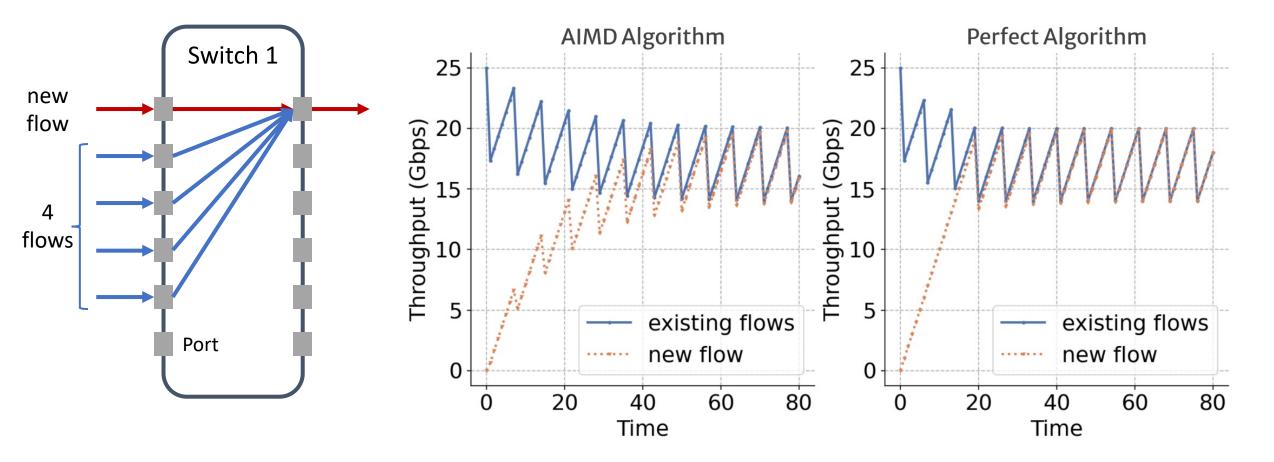
Design PrinciplesMotivationKey IdeaDesignEvaluation

Motivation 1: React to Every Congestion -> Not Max-min Fairness



HPCC & DCTCP react to every congestion, so the victim flow does more MD operations.

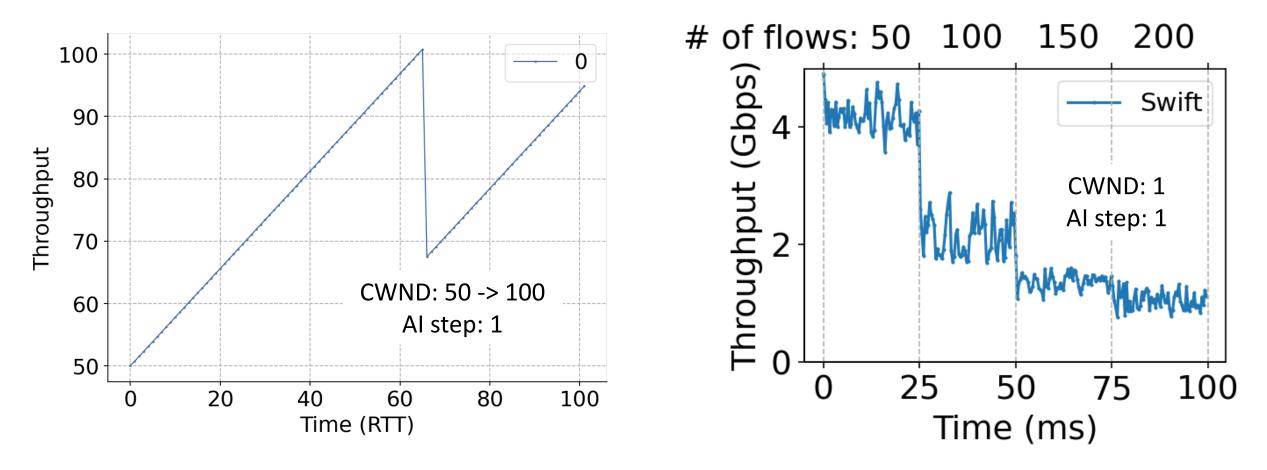
Motivation 2: Decrease rate below fair-share -> slow convergence



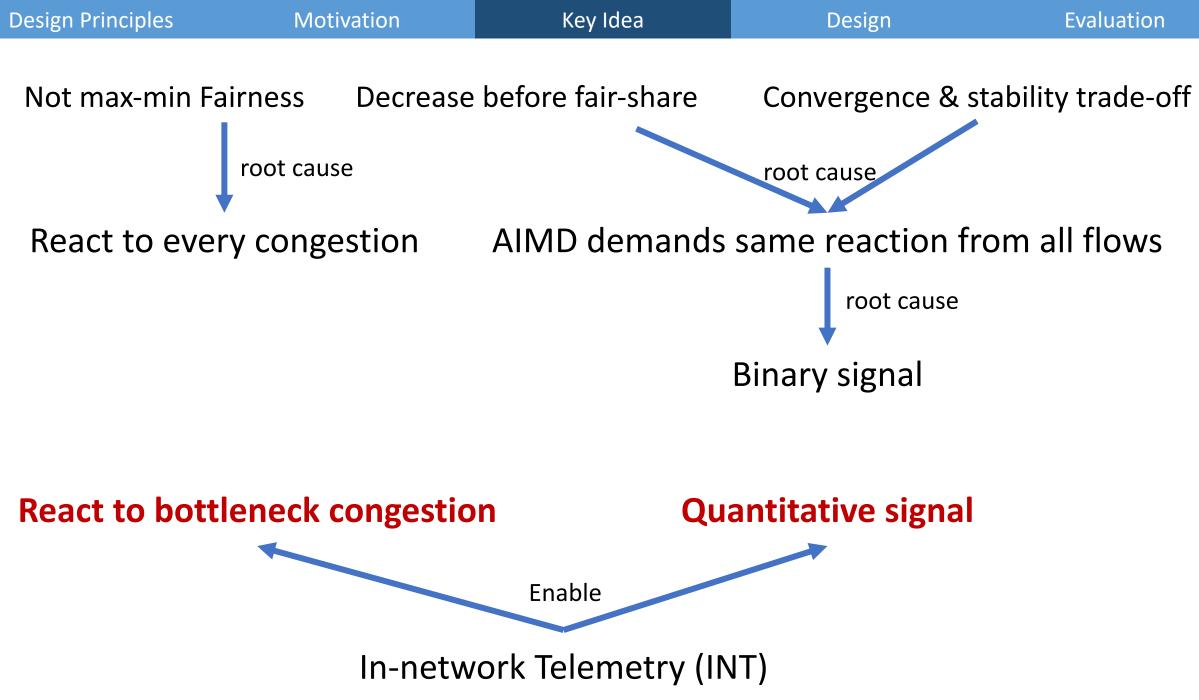
The flow that haven't reached fair-share should not decrease rate.

Key Idea

Motivation 3: Convergence Speed & Stable Rate Trade-off

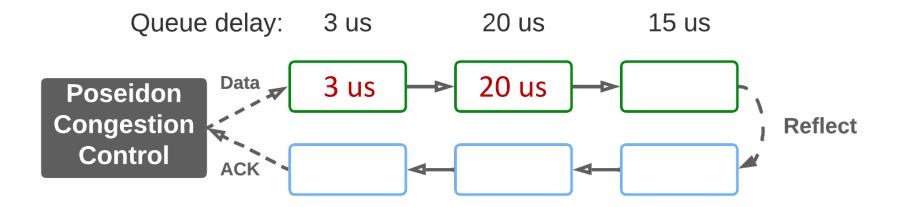


AIMD uses fixed AI step, so it cannot achieve both fast convergence and stable rate enforcement.



### Design 1: A Practical Low-overhead Quantitative Signal

- Signal: maximum per-hop delay (MPD)
  - Fixed short length: 2 bytes
  - Collected along the forwarding path
  - Reflected to sender through ACK



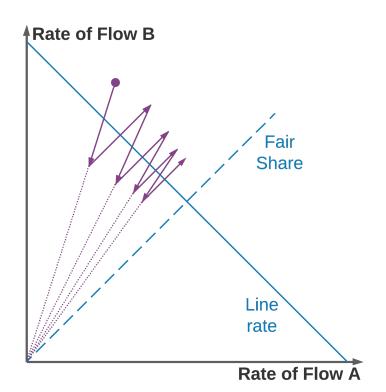
## Why does Existing CC with INT Have the Same Problems?

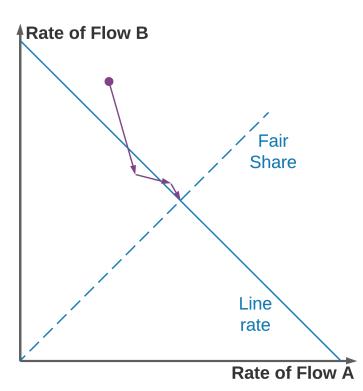
They still uses same idea as AIMD

Either all flows increase, or all flows decrease

Poseidon decouples from AIMD

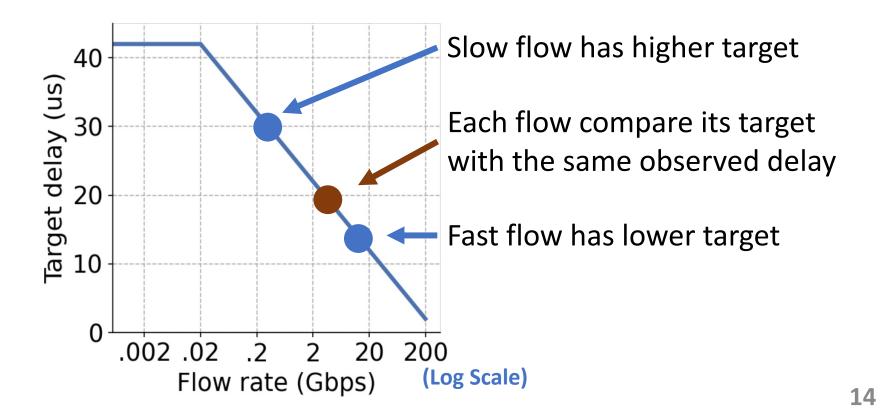
Every flow **reacts differently**, Some increase, some decrease.





Design 2: Rate-adaptive Target Enables Different Reactions

- Each flow calculates its own max per-hop delay target (MPT)
  - MPT = T(rate)
  - larger rate -> smaller target

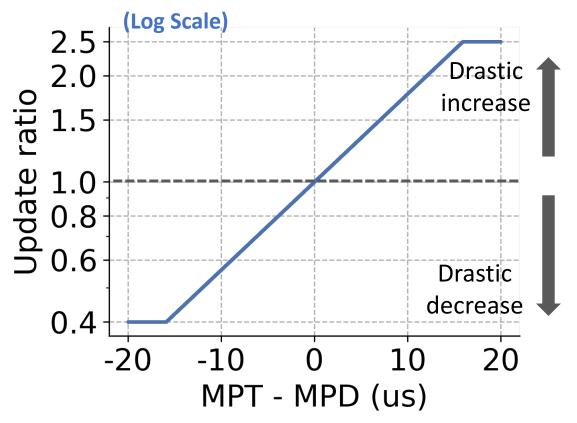


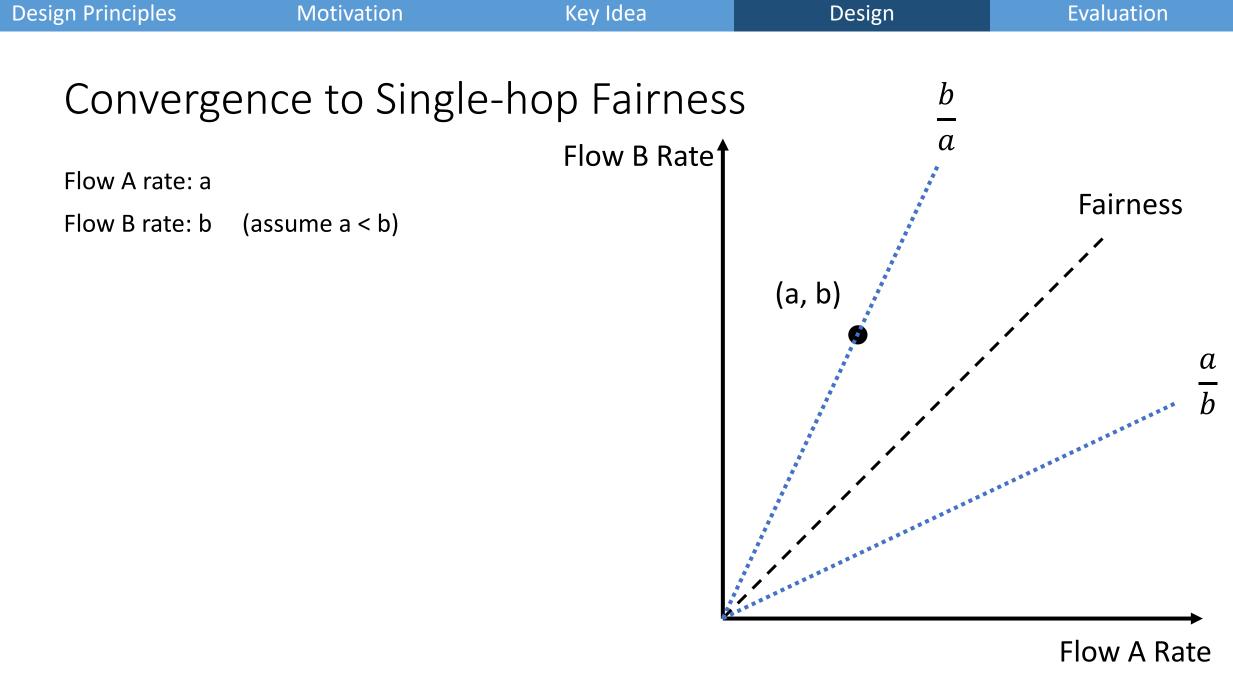
Key Idea

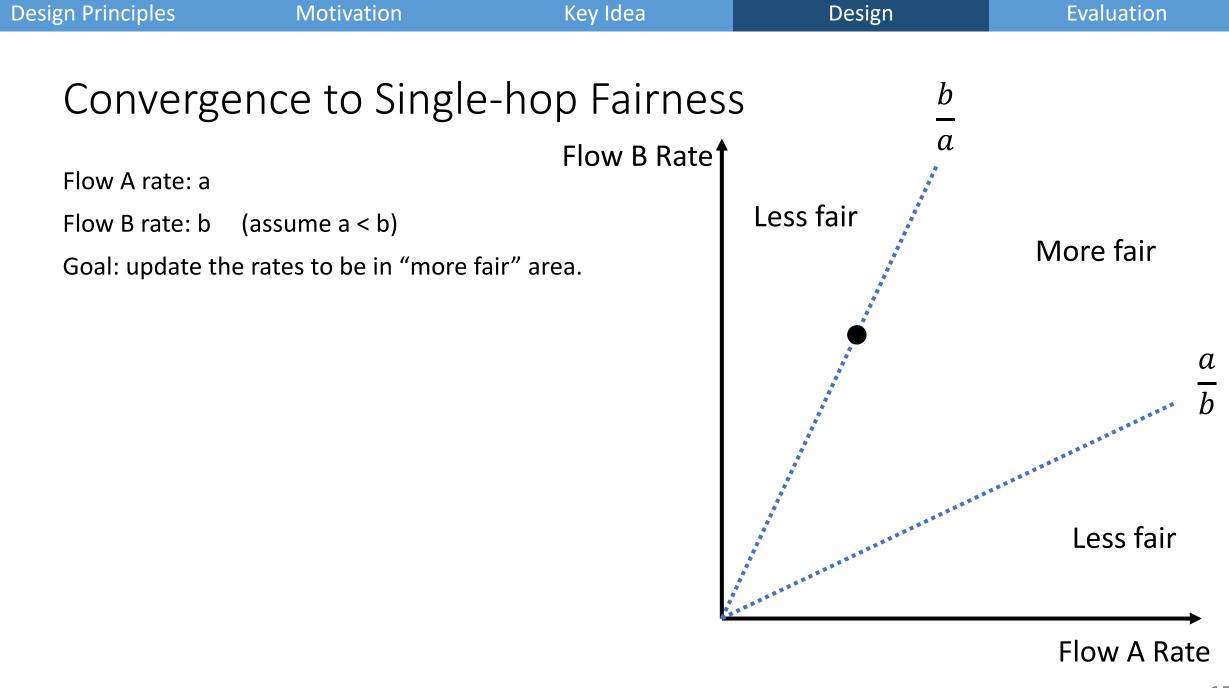
## Design 3: Adaptive MIMD Rate Update

- Each flow updates rate multiplicatively (MIMD)
  - update\_ratio = U(MPT, MPD)
  - new\_rate = rate \* update\_ratio

- MPT < MPD, decrease
  - MPT << MPD, decrease more drastic
- MPT > MPD, increase
  - MPT >> MPD, increase more drastic







Design Principles	Motivation	Key Idea	Design	Evaluation		
<b>C</b>			- b			
Convergence to Single-hop Fairness <u>b</u>						
		Flow B Rate	a			
Flow A rate: a						
Flow B rate: b	(assume a < b)		Less fair 🏑	_		
Goal: update the	e rates to be in "more fa	air" area.		More fair		
Given any delay	D, the rate updates are:	:		' h') a		
	$a' = a \cdot U(T(a), D)$		(a	, , , , , , , , , , , , , , , , , , , ,		
	$b' = b \cdot U(T(b), D)$			<b>b</b>		
To guarantee co	nvergence:		A REPERT	*****		
	$\frac{a}{b} < \frac{b'}{a'} < \frac{b}{a}$			Less fair		

Flow A Rate

## Convergence to Single-hop Fairness

Flow A rate: a

Flow B rate: b (assume a < b)

Goal: update the rates to be in "more fair" area.

Given any delay D, the rate updates are:

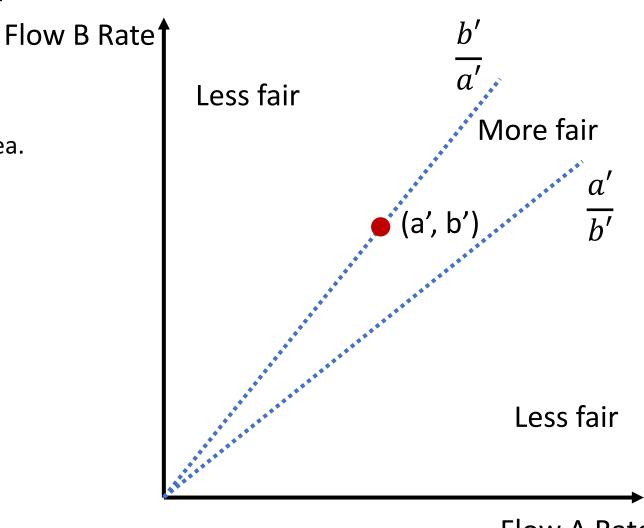
 $a' = a \cdot U(T(a), D)$  $b' = b \cdot U(T(b), D)$ 

To guarantee convergence:

$$\frac{a}{b} < \frac{b'}{a'} < \frac{b}{a}$$

Repeat until converge.

Note: The complete proof with corner cases discussion is in the paper.

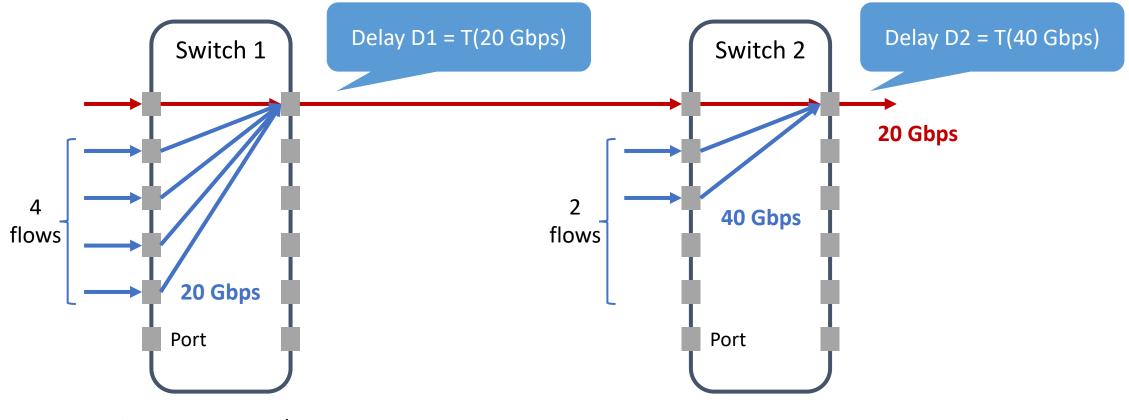


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Design Principles	Motivation	Key Idea	Design	Evaluation		
Convergence to Single-hop Fairness						
Flow A rate: a Flow B rate: b	(assume a < b)	Flow B Rate	Less fair	$\frac{b'}{a'}$		
·	the rates to be in "more fair or D, the rate updates are: $a' = a \cdot U(T(a), D)$ $b' = b \cdot U(T(b), D)$	Any update fun target function need to satisfy	Т()	fair $\frac{a'}{b'}$		
To guarantee co	$\frac{a}{b} < \frac{b'}{a'} < \frac{b}{a}$			Less fair		
Repeat until converge.				Flow A Rate		

Note: The complete proof with corner cases discussion is in the paper.

### Convergence to Max-min Fairness in a Network



Line rate: 100 Gbps

#### Red flow's MPD = max(D1, D2) = D1

The bottleneck always has the largest delay. We proved this leads to max-min fairness.

## Implementation

- Testbed
  - Implementation
    - 2 lines of core P4 code to obtain INT signal
    - Small changes to Swift algorithm in **Pony Express**
  - Topology
    - 2 hosts (virtualized into 16 hosts) + 2 Tofino-2 switches
- Simulator
  - Customized OMNeT++ packet simulator
  - Topology
    - Clos network with 64 racks

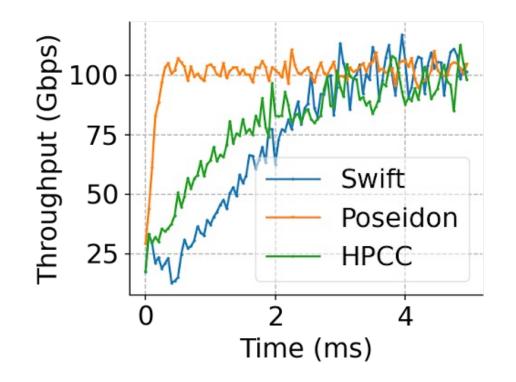
## Evaluation Summary

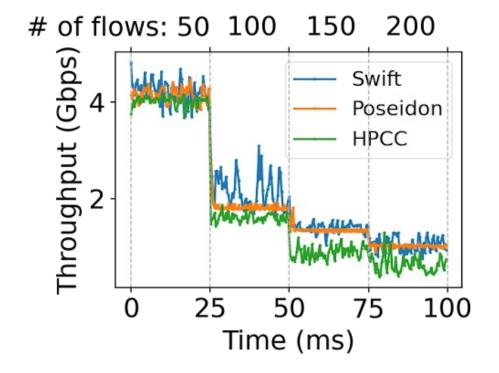
- Efficiency
  - **12x** faster convergence
  - 24x more stable throughput
  - 3x lower RTT
  - Full utilization
  - 1.78x faster median and 27x faster tail op latency (FCT)
- Robustness max-min fairness
  - Max-min fair in multi-hop congestion
  - Max-min fair in **reverse-path** congestion
- Practical
  - Implementation on production networking stack with no NIC changes
  - Incremental gain for incremental deployment
  - Bounded unfairness during partial deployment

Key Idea

Design

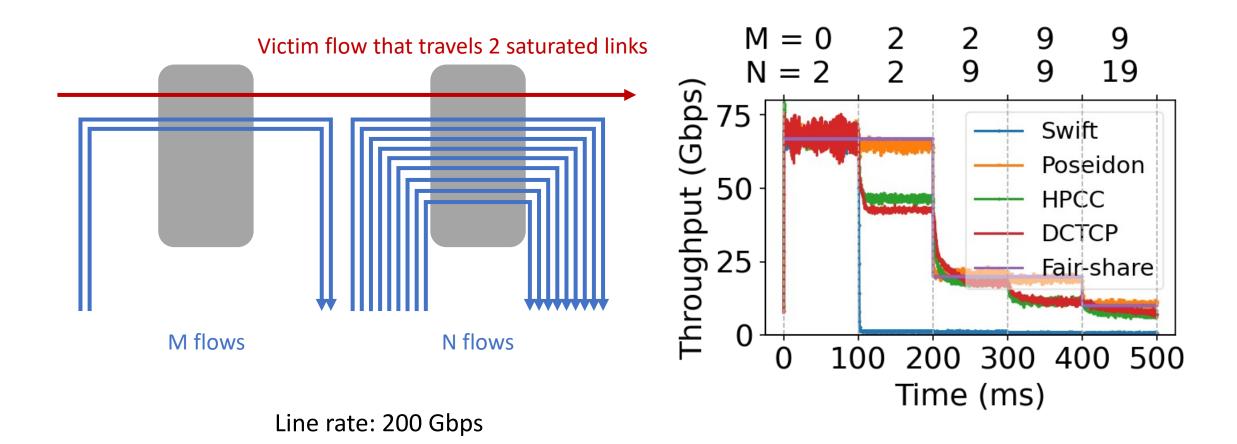
## Fast Convergence and Stable Throughput





**12x Faster Convergence** Faster multiplicative increase. Ramp-up without any decrease. **24x More Stable Throughput** Do not need additive increase. Update U() = 1.0 after converge.

## Poseidon Achieves Max-min Fairness

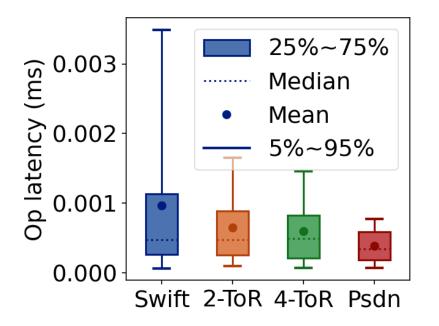


Poseidon achieves max-min fair rate for all flows, including the victim flow.

## Performance Gain for Incremental Deployment

4 racks send traffic to each other

- Swift: baseline with Swift CC
- 2-ToR Poseidon: 2 ToR switches support INT
- 4-ToR Poseidon: 4 ToR switches support INT
- Poseidon: all switches support INT



Performance improves as more switches support INT feature.

## Conclusion



- Poseidon algorithm uses quantitative per-hop INT:
  - Decouples fairness from AIMD
    - Gives a cluster of functions that can achieve fairness
    - Picks adaptive MIMD algorithm for outstanding performance
  - Achieves max-min fairness
    - Multi-hop congestion & reverse-path congestion
  - Supports incremental deployment
    - Performance improves when only ToR switches provide INT
- Poseidon is now open-sourced in ns-3 (developed based on the paper)
  - <u>https://github.com/Clark5/Poseidon</u>