#### Disaggregating Stateful Network Functions

Deepak, Gerald, <mark>Rishabh</mark>, Michal, James, Silvano, Mario, Krishna, Arun, Arunkumar, Balakrishnan, Avijit, Sachin, Deven, Evan, Pranjal, Rishiraj, Neeraj, Soumya, Stewart, Ranveer, Srikanth





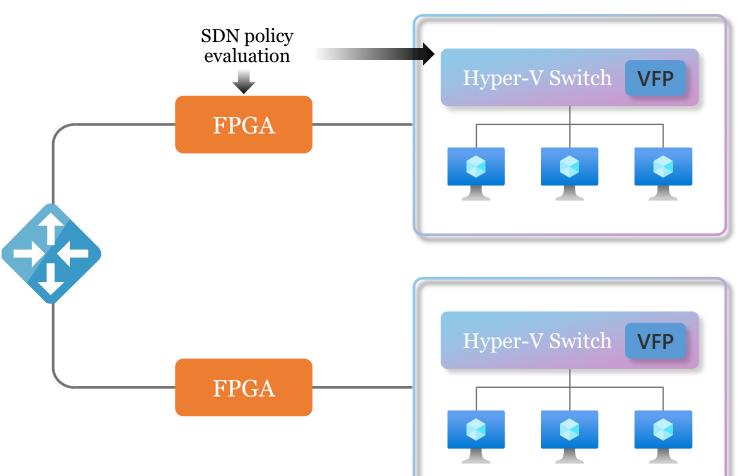


#### Azure SDN

Network policy processing today is performed on host node in virtual switch

Demanding customers use high number of SDN rules

SDN policy evaluation can be resource intensive when configurations are complex



#### Key motivation to Disaggregate the SDN

Enable full feature parity at high scale for **non VM workloads** 

• Hardware storage appliances (NETAPP), Super computers (Cray), on-prem, hypothetical NICs corresponding to storage accounts.

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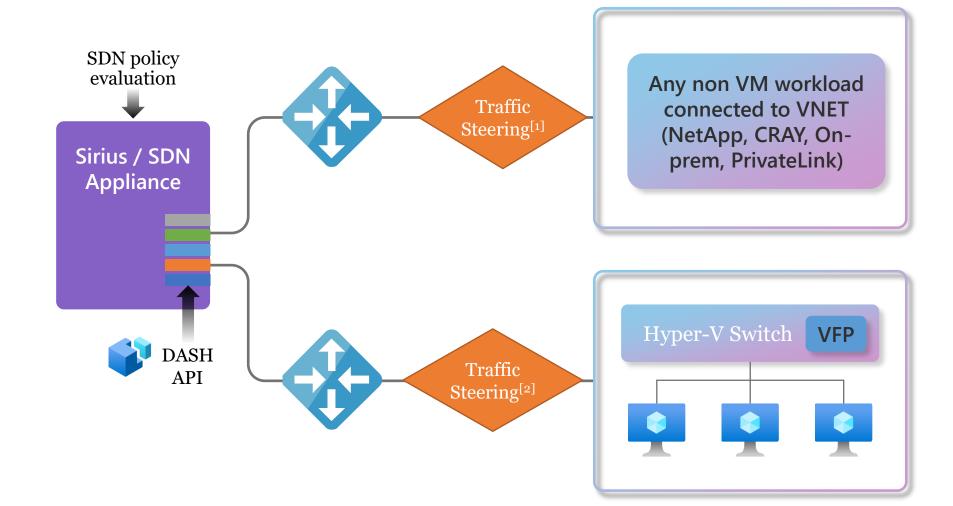
Enable **network optimized VMs** which require higher flow scale than can be supported on one host.

### Azure SDN with Sirius (SDN appliances)

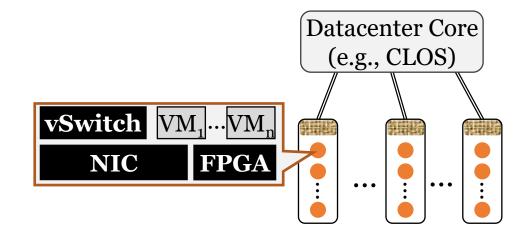
Offload SDN policy evaluation to disaggregated appliances

Greater agility, policy scale, flow scale and CPS

Enables rich scenario set beyond VM use cases.

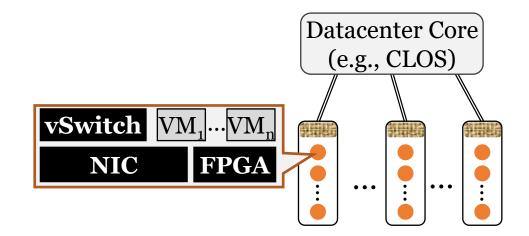


[1] VXLan Tags at switch[2] As above, with an FPGA match-action rule



Example Stateful NFs:

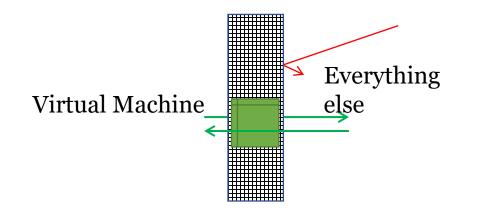
- Connection-tracking firewall
- Azure PrivateLink
- Stateful load balancers ...

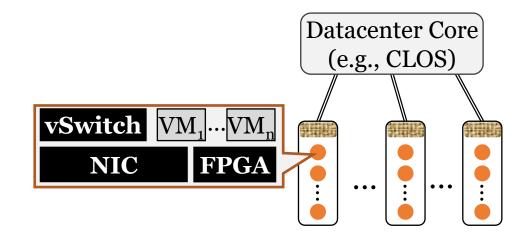


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#### **Connection-tracking firewall**





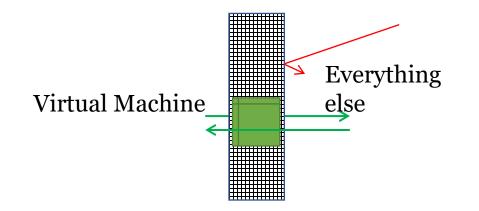
Functionality:

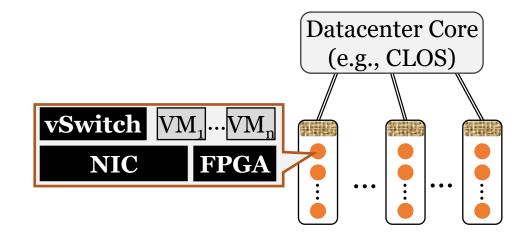
**Block** incoming packets except those that **belong to connections initiated by the VM** 

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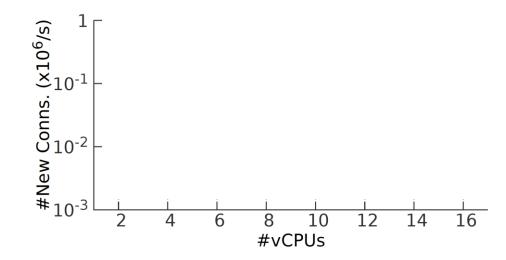
State:

Proportional to **#Connections** 

Perf impact: Connection **scale** and **latency** 

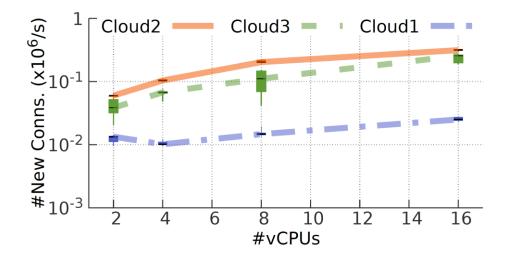
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<u>Method:</u> Pair of VMs. TCP connections as-fast-as-you-can with one connection-tracking firewall rule

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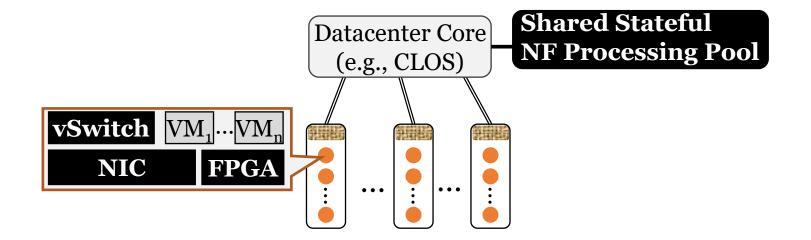
Pair of VMs.

TCP connections as-fast-as-you-can with one connection-tracking firewall rule

No cloud achieves more than 300K CPS

#### Disaggregating stateful NF processing

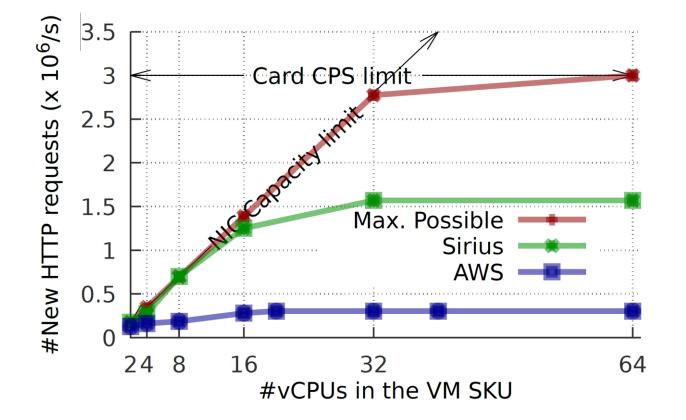
### Disaggregating stateful NF processing



Floating NICs that onboard onto Sirius have their stateful NFs processed in a disaggregated pool of (Pensando) cards

## With Sirius, connection scale increases to over 5X the closest other cloud

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TRex, ~250B request and response. Note: on AWS, we use c5n.18xlarge

#### **Disaggregating Stateful NFs** has value beyond perf.

Agility New features from Sirius can be used by VMs on any hardware SKU. Cost Inexpensive FPGAs at host may suffice since disaggregated pool handles all the spillover load. Vendor-neutral

Can use best-of-class implementations of the appliance.

**1. Performant** stateful NF processing on the cards

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P4 programmable MPU pipelines + ARM cores + coherent large DRAM

Per card: <u>3M CPS</u>, <u>16M conn.</u>, <u>2x100Gbps</u>

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2. Ensure high availability of state inspite link, switch and **card failures** 

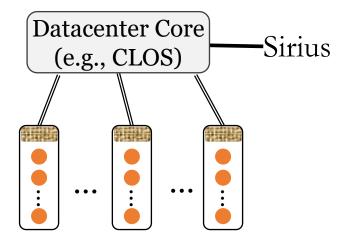
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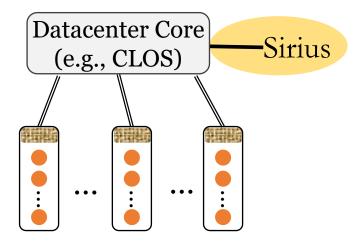
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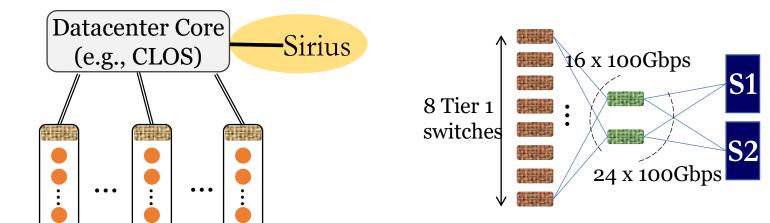
Per card: <u>3M CPS</u>, <u>16M conn.</u>, <u>2x100Gbps</u>

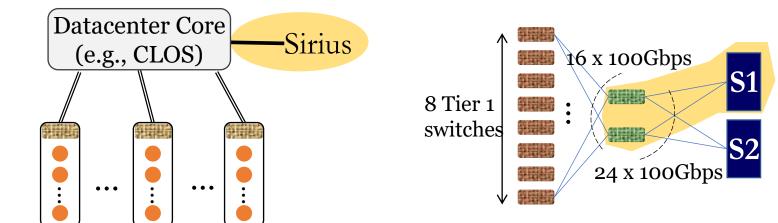
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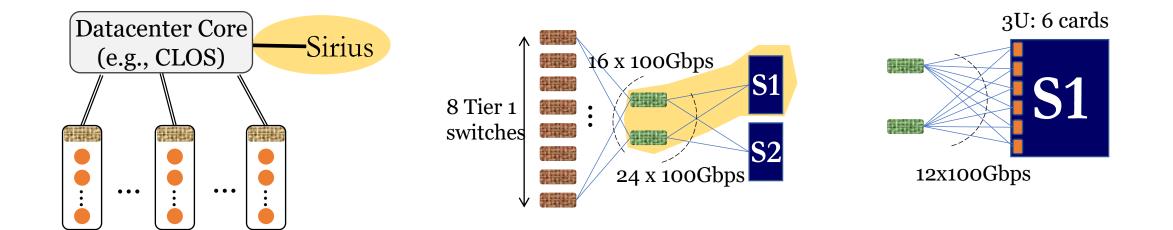
Redundant paths, cards, ... and in-line state replication (Paxos-style does not scale to packet state)

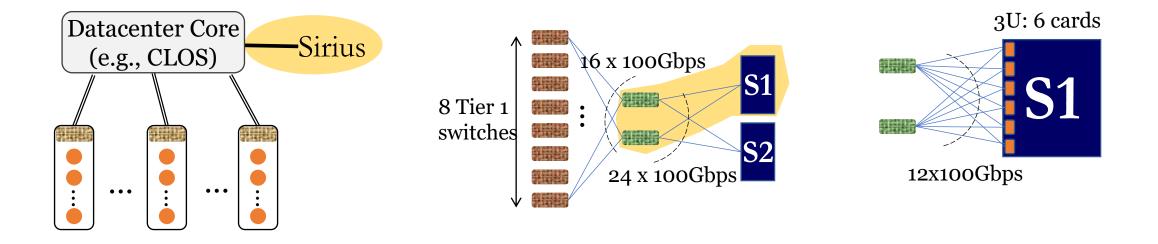




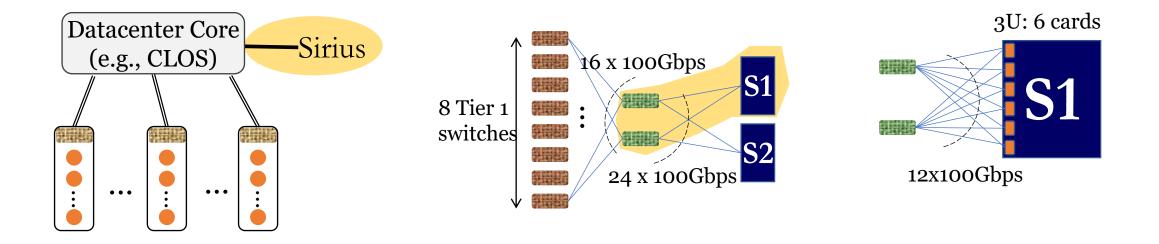








Availability Access to state (at the cards) and NF processing capability remains as long as:

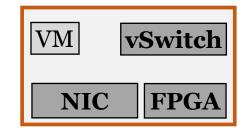


Availability Access to state (at the cards) and NF processing capability remains as long as:

- 1. At most one of the two links connecting each card to green switches fail
- 2. At most one of the green switches fail
- 3. At most half of the links connecting green switches to the red switches fail
- 4. At most half of the red switches fail

#### Disaggregation Datapath Versus Direct Datapath

Direct (AccelNet)

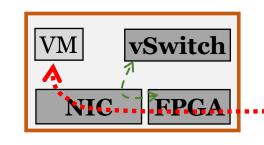




State in FPGA First packets go to vswitch

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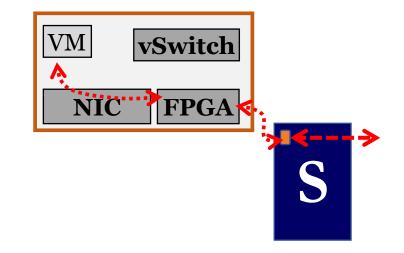
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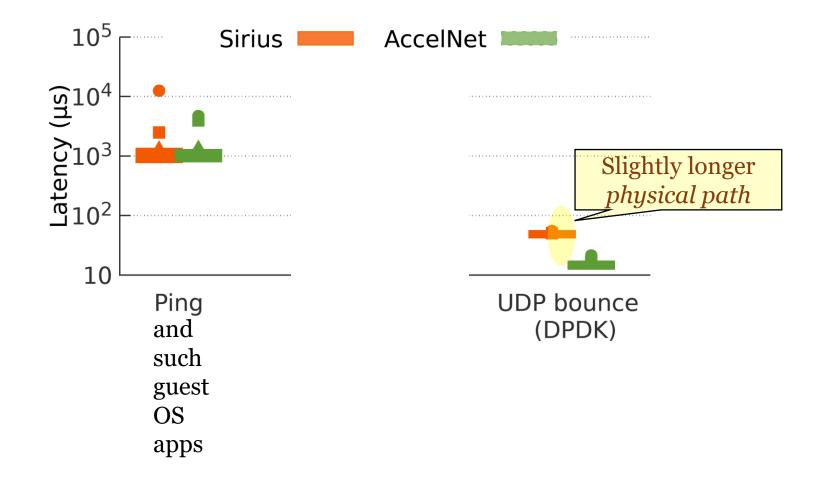
Disagg. (Sirius)

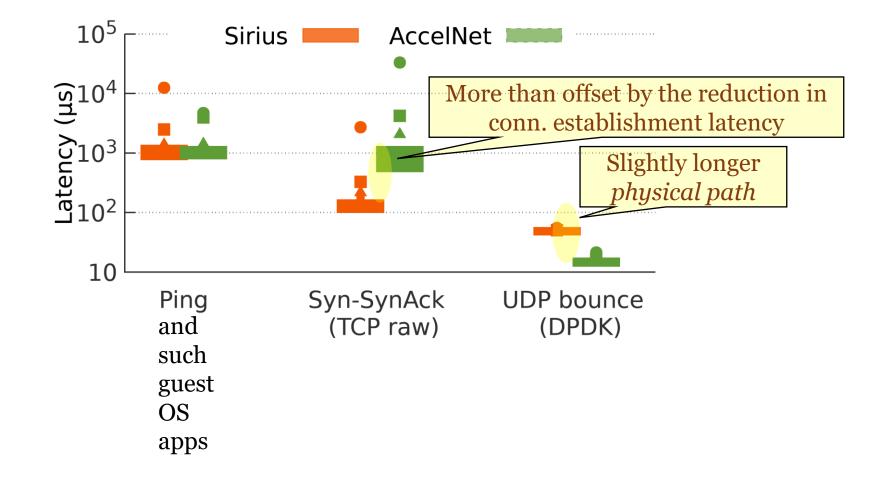


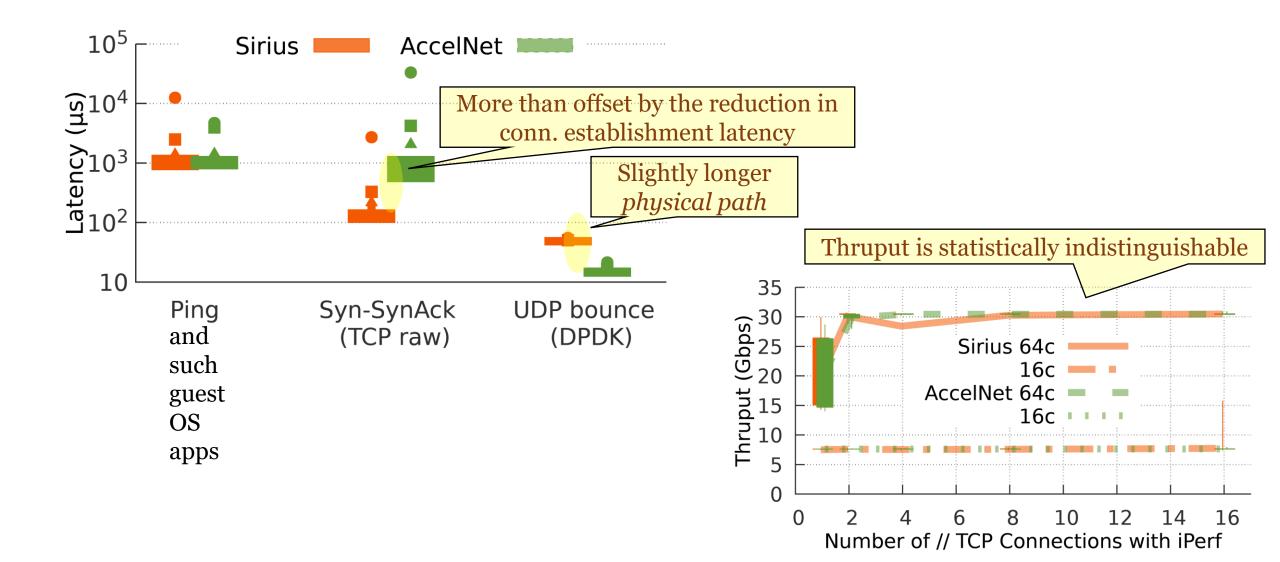
Sirius card announces address of fNIC

FPGA tunnels packets to the Sirius card

10<sup>5</sup> AccelNet Sirius Latency (µs) 10<sup>3</sup> 10<sup>2</sup> 10 Ping and such guest OS apps

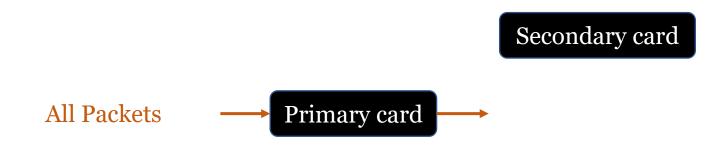


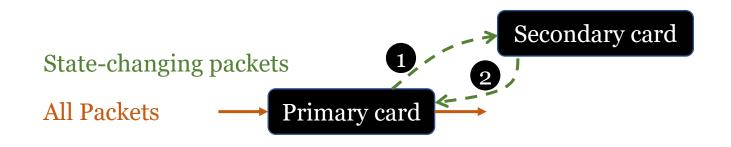




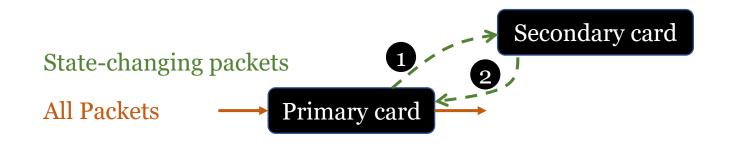








If a card fails, state on that card will be lost. So, we replicate state.



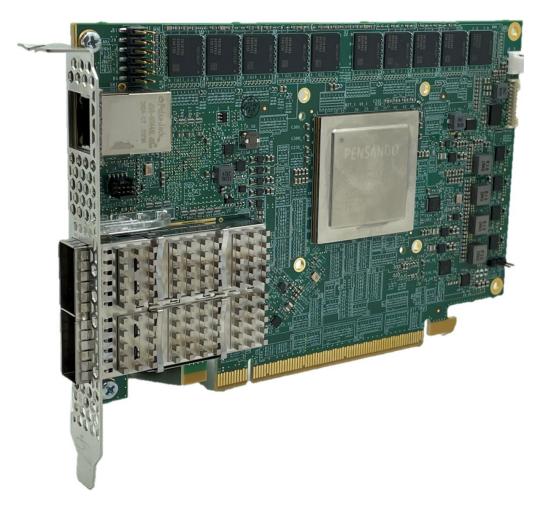
**In-line State Replication** 

Nuances in the paper Key properties:

- Safety
- Performant
  - No additional buffering of packets during replication
  - Card pairs are located on nearby appliances

## Performant stateful NF processing

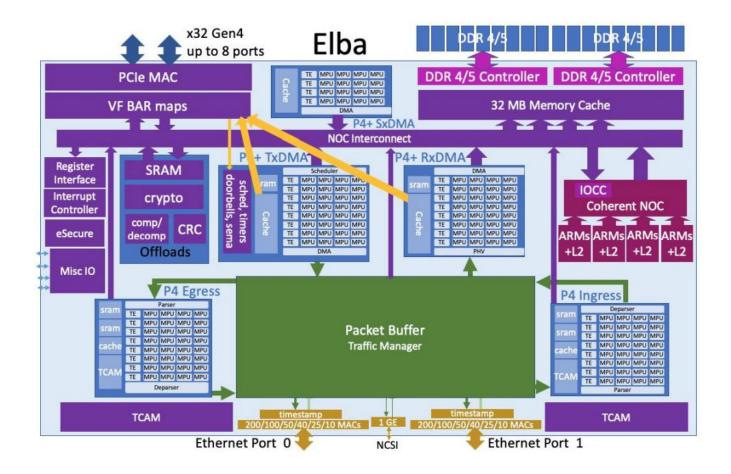




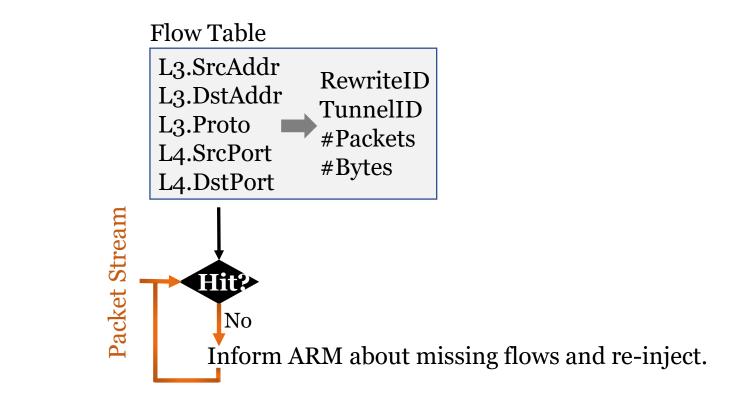
## Performant stateful NF processing

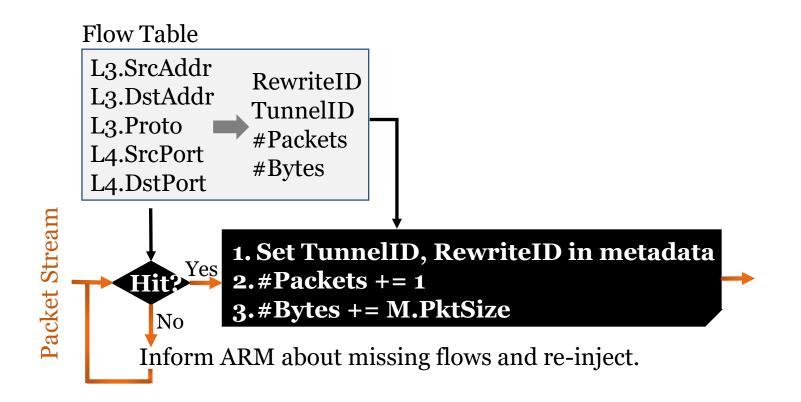
P4 programmable MPUs for normal packet processing

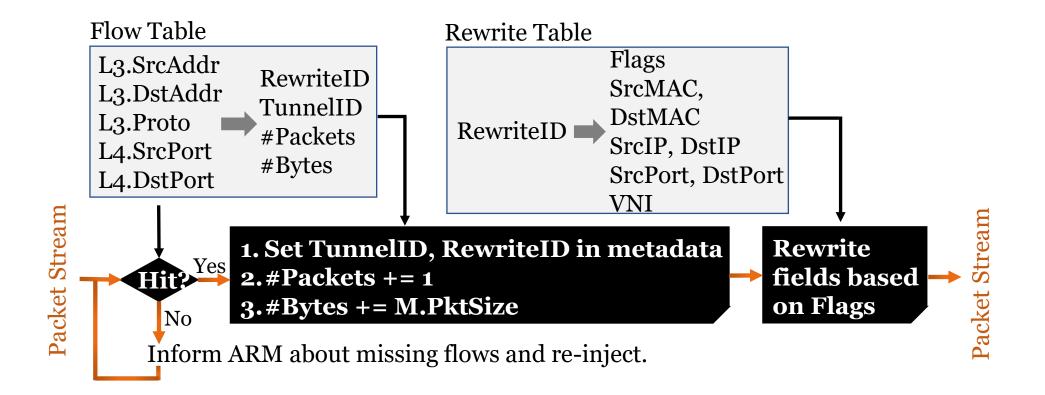
Novel MPUs that use state in the DRAM in packet processing



Flow Table L3.SrcAddr L3.DstAddr L3.Proto L4.SrcPort L4.DstPort





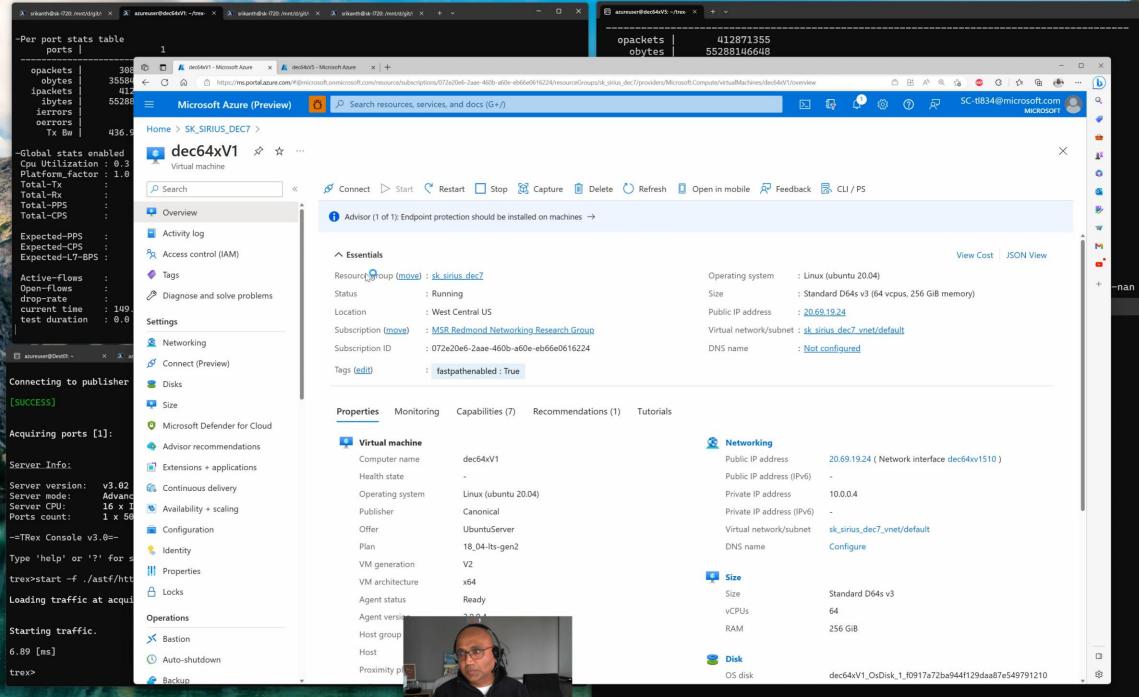


## Other aspects

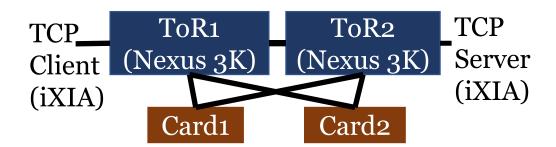
- Measurements of production NF load at Azure
- Control plane:
  - Failing over to secondary when primary is unreachable
  - Allocating fNICs to cards
- Splitting an fNIC's load between multiple cards
- On-demand spillover of fNIC from FPGA to Sirius (e.g., at load)
- Open APIs (DASH) to invite other implementations

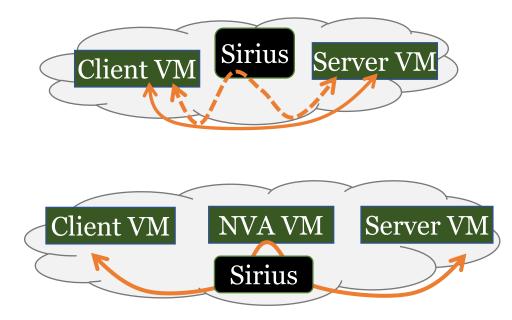
#### Status and Demo

#### Sirius is in Public Preview at Azure and on track for GA



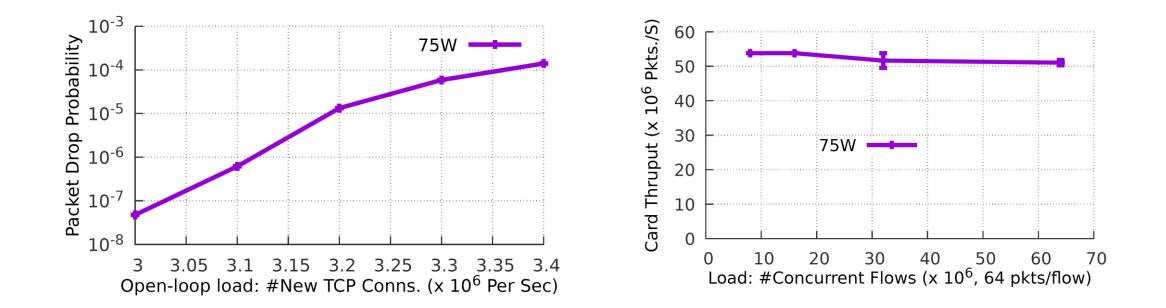
#### Evaluation





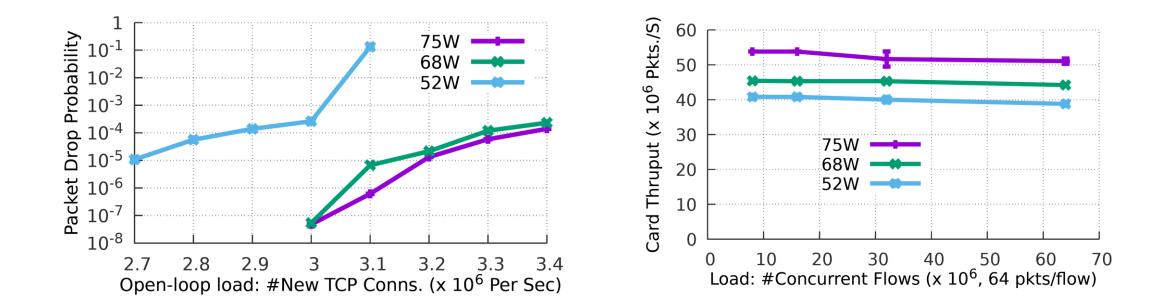
In Azure

#### Performance in the lab

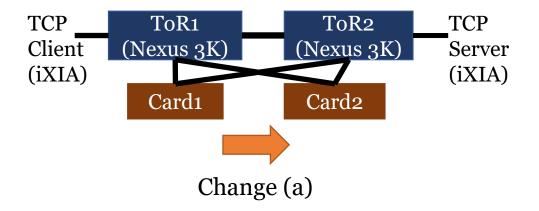


Each card supports high CPS, large numbers of concurrent flows (and PPS)

#### Performance in the lab



Each card supports high CPS, large numbers of concurrent flows (and PPS) Reducing power appears possible



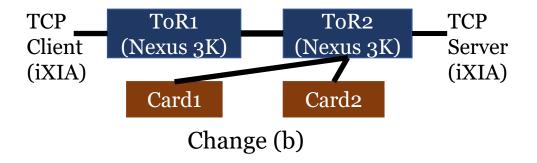
#### Change

(a) Planned switchover

(b) ToR1's links to

both cards are down

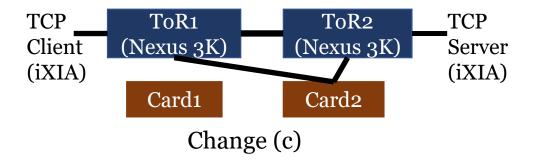
(c) Card1's links to both ToRs are down



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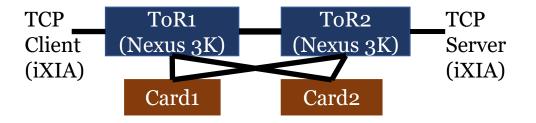
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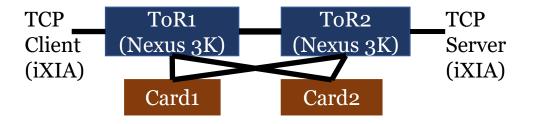
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Change	#Flow breaks	% of pkts dropped All At Cards		Recovery Latency
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Change	#Flow	% of pkts dropped		Recovery
	breaks	All	At Cards	Latency
(a) Planned switchover	0	0.00316%	0	1.89ms
(b) ToR1's links to	0	0.00929%	0.0000227%	5.75ms
both cards are down				
(c) Card1's links to	0	0.00835%	0.0000201%	5.01ms
both ToRs are down				

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Stateful NF processing is resource-intensive and a key bottleneck Disaggregating stateful NF processing saves \$, offers higher perf, allows laissez-faire deployment

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Novel techniques

- Splitting functions between ARMs, packet-MPUs and MPUs that access DRAM
- In-line state replication
- Efficient failover
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State-of-the-art performance available at Azure in <u>preview</u>.