

# Dynamic Hooks

## Hiding Control Flow Changes within Non-Control Data

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# Background & Motivation

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- Event interception requires us to divert the control flow at runtime
- This is accomplished by installing **hooks** into the control flow

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- ▶ Achilles Heel: Hooks

- ▶ Types

- ▶ Change code (Code Hooks)

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⇒ **How can we evade existing detection mechanisms?**



# Background & Motivation

- ▶ Hook Detection

## Assumption

- ▶ Hooks must target **persistent** control data

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**Dynamic Hooks: Evade existing mechanisms by targeting **transient** control data**

# Outline

- 1 Background & Motivation
- 2 Dynamic Hooks**
- 3 Experiments
- 4 Limitations
- 5 Conclusion

# Dynamic Hooks

▸ Idea

▸ Apply **exploitation techniques** to the problem of hooking

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⇒ **Target transient control data**

⇒ **No evident connection between hook and control flow change**



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- ▶ We can modify **internal** data structures and attack **internal** functions
- ▶ We can **prepare** our shellcode in advance

⇒ **Much stronger attacker model**

# Dynamic Hooks

## ▶ Example: Linux

```
1 struct list_head
2 {
3     struct list_head *next;
4     struct list_head *prev;
5 };
6
7 static void list_del(struct list_head *entry)
8 {
9     entry->next->prev = entry->prev;
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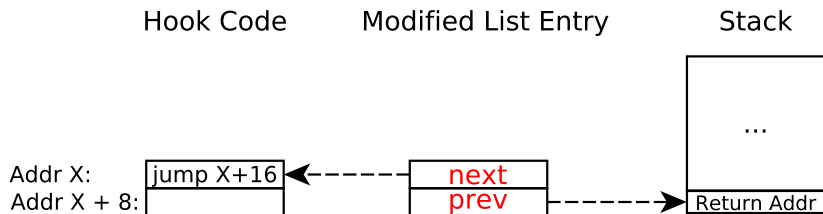
## write-where-what

- ▶  $[next + 8] = prev$
- ▶  $[prev] = next$



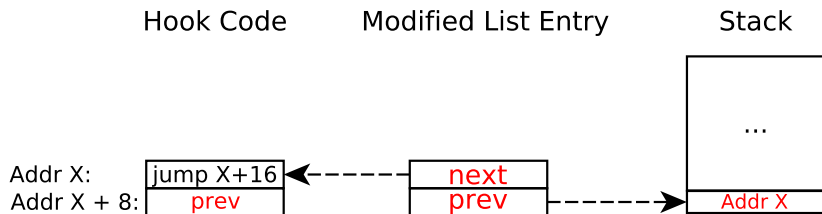
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```
mov [rax], rbx
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- Dynamic **data** hooks

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- Finding Dynamic Hooks

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- `mov [<destination>], <source>`
- backwards breadth-first search on the assembly-level
- extract path if destination and source originate from a global variable

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## ▸ Finding Dynamic Hooks

### ▸ **Program Slicing**

- `mov [<destination>], <source>`
- backwards breadth-first search on the assembly-level
- extract path if destination and source originate from a global variable
- Implementation: Based on IDA Pro

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- ▶ transform extracted path into VEX IR (pyvex)
- ▶ map VEX statements into Z3 expressions
- ▶ check satisfiability of conditional branches



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- Program Slicing
- **Symbolic Execution**
  - transform extracted path into VEX IR (pyvex)
  - map VEX statements into Z3 expressions
  - check satisfiability of conditional branches
  - generate detailed information about controlled registers

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# Experiments

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<i>OS</i>	<i>Instructions</i>	<i>8-byte moves</i>	<i>Slices</i>	<i>Paths</i>
Linux	1,976,441	42,130	1753	<b>566</b>
Windows	1,330,791	26,694	5450	<b>379</b>

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## Prototype Limitations

- Program Slicing: no memory model  
⇒ **79,853** paths ignored

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## Prototype Limitations

- Program Slicing: no memory model  
⇒ **79,853** paths ignored
- Symbol Execution: supports only a subset of x86 instruction set  
⇒ **5857** slices ignored

# Experiments

## ▶ Automated Path Extraction

### **Implemented three prototypes of dynamic hooks**

- 1 Control Hook: Interception of system calls (Linux)
- 2 Data Hook: Backdoor (Linux)
- 3 Control Hook: Interception of process termination (Windows)

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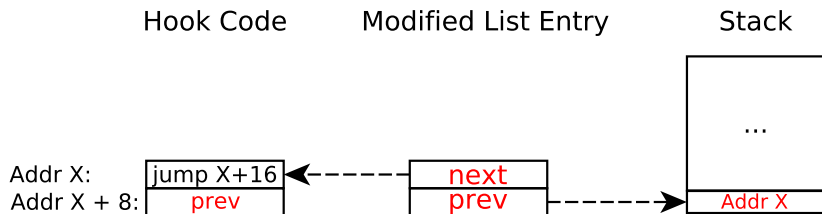
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- ▶ Side effects?

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- ▶ are more complex than traditional hooks
- ▶ are more fragile than traditional hooks